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**Remark**

Applicants respectfully request reconsideration of this application as amended.

Claims 6, 11, 12 and 13 have been amended. No Claims have been cancelled. Therefore, claims 1-32 are present for examination.

**35 U.S.C. §112 Rejection**

The Examiner has rejected claims 6, 11, 12 and 14 under 35 U.S.C. §112, second paragraph, for failure of antecedent basis. Claims 6, 11, 12, and 14 are amended.

**35 U.S.C. §102 Rejection**

*Shinmori*

The Examiner has rejected claims 1-5, 7-10, 13 and 15-32 under 35 U.S.C. §102 (e) as being anticipated by Shinmori et al., U.S. Patent No. 7,058,856 ("Shinmori"). Shinmori describes a microcontroller with a JTAG port to allow an on-die ROM to be updated.

Claim 1 recites, "a logic structure integrated in an integrated circuit (IC), the logic structure having

a set of bus inputs to generate events,  
a mask register to select inputs from among the set of bus inputs,  
a logic register to select logic to apply to the selected inputs and  
an event output to supply the result of the applied logic."

The Examiner purports to have found these structures in Shinmori in the CPU and ROM. The Examiner has not specifically explained how the claim is being read onto

Shinmori. However, there typically is not and Shinmori does not disclose a mask register in a processor to select inputs from among a set of bus inputs.

If the mask register is to be found in a ROM, then there typically is not and Shinmori does not disclose a set of bus inputs to generate events, nor a logic register to select logic to apply to the selected inputs. Bus inputs are found, but events are not generated by ROM and logic is not applied to selected inputs.

Claim 1 further recites, "a bus interface integrated in the IC and coupled to the logic structure to transmit settable parameters to the mask register and the logic register of the logic structure from an external agent."

The Examiner would appear to now be introducing the JTAG port. However, the Examiner has not specifically explained where the mask register is that is controlled externally through the JTAG port. As recited in the claim, the mask register selects bus inputs and logic is then applied to the inputs. Shinmori describes that the JTAG port allows the security bit to be changed and in the cited section the JTAG port controls the JTAG selector. However, there is no suggestion that an unidentified mask register and logic register be controlled through the JTAG port.

Absent any teaching or suggestion of these specific limitations in the reference, Applicants submit that Claim 1 is allowable over the reference. The Examiner has made essentially the same rejection to the other independent claims and, for example, has found no specific teaching of the counter or system management interrupt in those claims.

Accordingly, the other independent claims are believed to be allowable on the grounds provided above. The remaining claims are dependent and are believed to be

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allowable on the grounds above as well as for the limitations set forth in each claim, respectively.

### **Claim Objections**

Claims 11, 12, and 14 are objected to for following proper claim numbering. This objection is not understood. There have been no previous amendments to the claims. These originally filed claims appear to be properly numbered. If the Examiner persists in this objection, then it is respectfully requested that the Examiner call the undersigned, so that a proper correction can be made.

### **Conclusion**

Applicants respectfully submit that the rejections have been overcome by the amendment and remark, and that the claims as amended are now in condition for allowance. Accordingly, Applicants respectfully request the rejections be withdrawn and the claims as amended be allowed.

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### Invitation for a Telephone Interview

The Examiner is requested to call the undersigned at (303) 740-1980 if there remains any issue with allowance of the case.

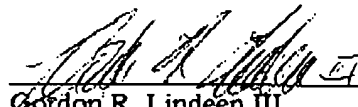
### Request for an Extension of Time

Applicants respectfully petition for an extension of time to respond to the outstanding Office Action pursuant to 37 C.F.R. § 1.136(a) should one be necessary. Please charge our Deposit Account No. 02-2666 to cover the necessary fee under 37 C.F.R. § 1.17(a) for such an extension. Charge our Deposit Account.

Please charge any shortage to our Deposit Account No. 02-2666.

Respectfully submitted,  
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